

## ABSTRACT OF THE DISCLOSURE

A secure LSI device **1** includes an encryption section **2** for encrypting a program, and an external I/F **50** for inputting/outputting a program or data from/to an external memory **100**. In the encryption section **2**, the operation of a private key arithmetic  
5 processing section **20** is prohibited with respect to a sequence whose execution is determined by a key-generation/update sequencer **30** to be impermissible. In the external I/F **50**, a program processing section **51** and a data processing section **55** are structured independently from each other.